



GAV 2509

PATENT  
ATTORNEY DOCKET NO. 06802/148001

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Im Cheol Ha Art Unit: 2509  
Serial No.: 08/998,157 Examiner: Trong Phan  
Filed : December 24, 1997  
Title : DECODER CIRCUIT IN A FLASH MEMORY DEVICE

Assistant Commissioner for Patents  
Washington, DC 20231

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TRANSMITTAL LETTER

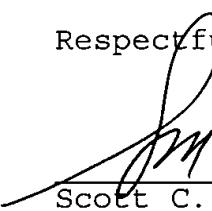
Correspondence relating to this application is enclosed.  
The required fees are computed below. Please apply any charges not  
covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	5	-	20	=	0	\$0
Independent	2	-	3	=	0	\$0

TOTAL FEE DUE: \$0

Respectfully submitted,

Date: 1/8/99

  
Scott C. Harris  
Reg. No. 32,030

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P.O. Box 927649  
San Diego, CA 92192

Telephone: 619/823-7778  
Facsimile: 619/272-1456

Date of Deposit 1-8-99  
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D.C. 20231.

Janet Christey  
(cont'd)

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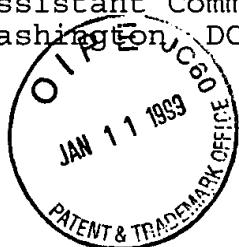
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*Arlene*  
1-19-99  
9/a



AMENDMENT

Sir:

In response to the Office Action dated October 9, 1998,  
paper no. 6, please amend the above-referenced application as  
follows:

In the Claims

Please amend claims 1, 3, 4 and 5 as follows:

1. (Amended) A decoder circuit in [a flash memory device]  
comprising[;]:

a global row decoder for outputting a global word line  
signal, said global row decoder consisted of a first decoding means  
selected according to a row address signal and a second decoding

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*Janet Christy*  
*Janet Christy*